



NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA
SPONSORED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION

NITR/SR/2022/Advt-19EE054/L/032, dt. 25.07.2022.

Applications are invited on prescribed format (On-line) for the temporary post of "Junior Research Fellow (JRF)" for the Project entitled "**Robot Operating System (ROS) Based Reconfigurable Flight Control Module Design for Multirotor Drones.**" Selected candidate will not be enrolled in Ph.D Programme in the **Department of Electrical Engineering, National Institute of Technology Rourkela** based on Institute norms and regulations.

1.	Project Code	SR/19/EE/054
2.	Name of the Temporary Post	Junior Research Fellow - 01
3.	Name of the Research Project	"Robot Operating System (ROS) Based Reconfigurable Flight Control Module Design for Multirotor Drones".
4.	Name of the Sponsoring Agency	SERB
5.	Tenure of the Project	03.12.19 to 02.12.22(The end date of the project may be extended may be extended)
6.	Tenure of the Assignment	Six month (Possibly will be Extended)
7.	Job Description	(i) Design and development of control algorithms for Quadcopter on ARM based microcontrollers (Flight controller) in a Real-Time-Operating system (RTOS) environment or Robot-Operating-System (ROS) environment. (ii) Design and development of auxiliary modules for flight controller. (iii) Assist the PI in preparation of reports related to the research work. Other works related to the project
8.	Consolidated monthly compensation / Fellowship	Rs. 31,000/- per month
9.	Essential Qualifications and experience	The applicants should hold a M.Tech/M.E degree in Electrical/Electronics/Computer Engineering or related specialization.
10.	Desirable Qualifications / Exp.	The applicant should have sound knowledge in microprocessor/microcontroller, control systems and image processing. Applicant should be able to write algorithms in Python/C++ or other languages. Understanding of basic engineering-mechanics is helpful. GATE/NET qualified candidate is preferable.
11.	Accommodation	Bachelor / Married accommodation in the Institute may be provided subject to availability.

For technical information on the project, the candidate may contact the Principal Investigator at the following address:

Name : **Prof. Asim Kr. Naskar**
Address : Department of Electrical Engineering.
National Institute of Technology Rourkela, Odisha 769 008.
Tel. No. : 0661-246 2408
Mobile : 9477008248
E-mail : naskara@nitrkl.ac.in

Date and Time for **On-line** Interview: **04.08.2022 at 17:00 HRS**

Place of Interview: **Online through google-meet (link will be communicated later by mail)**

Eligible candidates may apply within **01.08.2022, 2359 HRS**. The candidates are required to send the complete filled up application form (Soft copy) to naskara@nitrkl.ac.in The application form is available in the following link: [http://nitrkl.ac.in/oldwebsite/ Jobs_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64\(2\).pdf](http://nitrkl.ac.in/oldwebsite/Jobs_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64(2).pdf) which must be filled by candidates and also required to attach photocopies of all supporting documents, research papers (if any) etc.

Candidates will build a **Single PDF** file; it is to be sent to Principal Investigator (naskara@nitrkl.ac.in) **on or before 01.08.2022, 2359 HRS**. The candidates are also required to produce relevant documents mentioned in the application form [such as original of all mark sheets and certificates, research papers (if any), experience certificate (if any) etc.] at the time of interview / joining. Selection / Joining will be canceled in case of any suppression of information / document provided earlier.

Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project.

**Sd/-
Asst. Registrar (SR)**

Copy to:

- 1) All Heads of the Departments, NIT Rourkela for publication on departmental notice boards.
- 2) **Prof. Asim Kr. Naskar**, PI with a request to give wide publicity to this advertisement.
- 3) Head of the Department, Department of Electrical Engineering(EE).
- 4) Project file.