



IIT Ropar – Technology and Innovation Foundation for Agriculture and Water Technology Development Hub (AWaDH)

CIN: U80100PB2020NPL051623

A Technology Innovation Hub established by the Department of Science & Technology (DST), Government of India, in the framework of National Mission on Interdisciplinary Cyber-Physical Systems (NM – ICPS)

316/317, M.Visvesvaraya Block
Indian Institute of Technology Ropar
E-mail: awadh@iitrpr.ac.in

Call for the recruitment of a Project Associate (PCB Designer)

August 2022

Applications invited for Project Associate (PCB Designer) at IIT Ropar – Technology and Innovation Foundation.

Last Date for Application: September 20, 2022

IIT Ropar – Technology and Innovation Foundation (TIF) is a Section – 8 company founded to support the initiatives of recently established Technology Innovation Hub – AWaDH (Agriculture and Water Technology Development Hub) at the Indian Institute of Technology Ropar in the framework of National Mission on Interdisciplinary Cyber-Physical Systems (NM – ICPS) by the Department of Science and Technology, Government of India. The TIF supports the R&D in the domain of Agriculture and Water and provides incubation support to technology-based start-ups towards sustainable agriculture and the environment.

IIT Ropar – TIF is looking for an enthusiastic and resourceful individual(s) to be a part of our journey towards a sustainable future.

Job description: Project Associate (PCB Designer)

Location: IIT Ropar - TIF

No of vacancy: Two (One Fresher & one with minimum two years of experience)

Full time (on contract) position in IIT Ropar – Technology and Innovation Foundation (TIF), a section – 8 company: Initially for one year. It can be extended depending on the satisfactory performance.

Responsibilities

1. Develop design documents like schematics, assembly, and fabrication drawings.
2. Prototype assembly and testing.
3. Analyze and resolve design-related issues in a timely manner
4. Design finalization and send files to manufacturers.
5. Provide support to manufacturing and packaging teams when required.
6. Proficiency in Altium Designer is mandatory.

Qualifications

1. B.Tech/B.E. or M.Tech/M.E. in Electrical Engineering or equivalent qualification with NET (CSIR/UGC) or GATE qualification
2. **Candidates without NET/GATE qualification may also be considered as per DST rules on a reduced fellowship.**
3. One Post is for fresher and one post is for minimum 02 year of relevant experience holder. More experience is desirable.

Remuneration:

- **Fresher:** Rs. 31000/- per month plus HRA as per sponsoring agency norms.
- **Experience holder-** Rs. 35000/- per month plus HRA as per sponsoring agency norms.
- Suitable on-campus accommodation may be provided, based on availability.

To apply click here:

<https://docs.google.com/forms/d/1NdSRyoUXpKDG7JzGChrQsqRrw3goWVZ1xU8NmvG1-ac/>

For any query write to md.tif@iitrpr.ac.in

Last Date for receiving applications: September 20,2022

Terms and Instructions

1. Only shortlisted candidates will be contacted/informed through email.
2. TIF reserves the right to fill up the post, not to fill up the post, or cancel the advertisement in whole or part without assigning any reason. The company also reserves the right to place a limit on the total number of candidates to be called for written test/or interviews. The decision of the company in this regard will be final.
3. Documentary evidence in support of all educational and professional qualifications will be required to be produced when specified.
4. The company can verify all the documents submitted by a candidate before the appointment, at the time of appointment, or during the tenure of the service. In case it is detected that the documents submitted by the candidates are fake or the candidate has clandestine antecedents/background and has suppressed the said information, then his/her services shall be terminated.
5. If it is found at a later date that any information given in the application is incorrect/false the candidature/appointment is liable to be canceled/terminated.