

**CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE, PILANI (RAJ.)**

**Advt. No. PF-07/2022**

**Date: 13.09.2022**

**“Online Interview”**

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

**Online applications are invited from candidates for Engagement of Project Staff.**

**Details of Vacancies & Eligibility**

<b>Post Code</b>	<b>Project No.</b>	<b>Area/Group</b>	<b>Name &amp; Number of Vacancy</b>	<b>Qualifications</b>	<b>Age</b>	<b>Consolidated Emoluments per month (fixed)</b>
<b>2801</b>	GAP-6221	Integrated circuits & systems	Junior Research Fellow 01	B.E./ B.Tech/ M.Sc (Electronics/ Instrumentation/ Instrumentation & Control Systems/ Electrical Engg./ EEE/ CS/ CE) or equivalent.  <b>Desirable:</b> M.E./ M.Tech (Electronics/ Instrumentation/ Instrumentation & Control Systems/ Electrical Engg./ EEE/ CS/ CE) or equivalent.  <b>Special Skills Required:</b> Circuit and PCB Design on various tools, experience to work with different microcontrollers, front & backend development for web & android App. <b>Languages:</b> C, C++, Python, Java, CSS, HTML.	35 Years	₹31,000/- +HRA as per rules
<b>2802</b>	HCP- 0034 (W.P.- 3.5)	High-Frequency Devices and Systems Group	Project Associate-I 01	B.E./ B.Tech/ M.Sc (Electronics/ Physics/ MW Electronics/ Applied Physics/ Optics/ Laser/ Nano-technology/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ BioElectronics/ Microelectronics/ VLSI Design/ Instrumentation) of equivalent.  <b>Desirable:</b> Programming skills in Arduino, Circuit design, Simulation and Sensor integration, PCB design experience.	35 Years	*₹31,000/- +HRA as per rules <b>OR</b> *₹25,000/- +HRA as per rules
<b>2803</b>	HCP- 0034 (W.P.- 07)	Semiconductor Process Technology Group	Project Associate-II 01	B.E./ B.Tech/ M.Sc (Physics/ Applied Physics/ Optoelectronics/ Laser Physics/ Nano Technology/ E&C/ Electronics/ Radio Physics & Electronics) or equivalent having 02 Years' experience in Research & Development in Industrial and Academic Institutions or Science and Technology organisations and Scientific activities and Services.  <b>OR</b> B.E./ B.Tech/ M.Sc (Physics/	35 Years	*₹35,000/- +HRA as per rules <b>OR</b> *₹28,000/- +HRA as per rules

				Applied Physics/ Optoelectronics/ Laser Physics/ Nano Technology/ E&C/ Electronics/ Radio Physics & Electronics) or equivalent with valid NET/GATE score having 02 Years' experience in Research & Development in Industrial and Academic Institutions or Science and Technology organisations and Scientific activities and Services.		
2804	GAP- 3417	Integrated Circuits and Systems Group	Junior Research Fellow 02	B.E./ B.Tech/ M.Sc (ECE/ EE/ EEE/ Electronics Engg./ CSE/ Information Technology/ Data Science/ Instrumentation/ Electronics/ Electronics Science) or equivalent.	35 Years	₹31,000/- +HRA as per rules <b>OR</b> ₹25,000/- +HRA as per rules
2805	HCP- 0034 (W.P- 2.4)	Semiconductor Process Technology Group	Project Associate-I 01	B.E./ B.Tech/ M.Sc (ECE/ Optoelectronics/ Applied Physics/ Nanotechnology/ Electronics) or equivalent.  Desirable: M.E./ M.Tech (ECE/ Electronics & Instrumentation/ Microelectronics & VLSI/ Nanotechnology) or equivalent having working experience and domain knowledge in the area of Infrared Detector design and fabrication.	35 Years	₹31,000/- +HRA as per rules <b>OR</b> ₹25,000/- +HRA as per rules
2806	HCP- 0026 (T2.4)	Advanced Electronic Systems area	Project Associate-I 01	B.E. / B.Tech./ M.Sc (Electronics/ Instrumentation/ Instrumentation and Control System/ Power Electronics/ Electrical Engg./ EEE/ Mechanical Engg./ Embedded System/ Robotics/ Mechatronics) or equivalent.  <b>Desirable:</b> GATE/ NET qualified candidates. Experience in Control Sstem, Power Electronics, Embedded System, Programming in C/MATLAB, python, Data Science and Machine Learning etc.	35 Years	₹25,000/- +HRA as per rules
2807	GAP- 3248	Semiconductor Sensors and Microsystems	Project Associate-I	B.E. / B.Tech./ M.Sc (ECE/ Electronics Engg./ Physics/ nanotechnology/ Material Science/ Robotics/ Mechatronics/ Optoelectronics) or equivalent with valid NET/GATE Score.  <b>Desirable:</b> working experience or domain knowledge in simulation of COMSOL Multiphysics Software, material science, sensors development or device fabrication, etc.	35 Years	₹31,000/- +HRA as per rules

2808	HCP- 0044	High Power Microwave System	Project Assistant 01	B.Sc/ 03 Years' Diploma (Electronics Engg./ Electrical Engg./ Computer Engg./ Instrumentation) or equivalent.	50 Years	₹20,000/- +HRA as per rules
2809	GAP- 3247	Semiconductor Sensors and Microsystems Group	Senior Research Fellow 01	B.E./ B.Tech/ M.Sc (Physics/ Engineering Physics/ Nano-Technology/ Electronics/ ECE/ Mechatronics/ Instrumentation) or equivalent with CSIR/UGC NET or Valid GATE score and having 02 Years' of experience in Research & Development in Industrial and Academic Institutions or Science and Technology organisations and Scientific activities and services. <b>OR</b> M.Tech/M.E. (Electronics/ Nanotechnology/ Material Science/ Instrumentation/ RF/ Microwave/ ECE) or equivalent having CSIR/UGC NET or valid GATE score.	35 Years	₹35,000/- +HRA as per rules
2810	MLP- 0121 (Task 2)	High Power Microwave Systems	Project Associate- I 01	B.E./ B.Tech/ M.Sc (ECE/ Microwave/ Optoelectronics/ Physics/ Electronics) or equivalent.	35 Years	₹31,000/- +HRA as per rules <b>OR</b> ₹25,000/- +HRA as per rules
2811	HCP- 0042 (W.P.- 1.4)	Advanced Electronic Systems area	Project Associate-II 01	B.E. / B.Tech./ M.Sc. (ECE/ Computer Science/ IT/ Electrical/ Electronics) or equivalent having 02 Years' experience in Research & Development in Industrial and Academic Institutions or Science and Technology organisations and Scientific activities and Services.  Desirable: M.E./ M.Tech (CS/ IT/ ECE/ Robotics/ EEE/ Mechanical/ Mechatronics/ Instrumentation) or equivalent having working experience and domain knowledge of areas of AI algorithm development or circuit design.	35 Years	*₹35,000/- +HRA as per rules <b>OR</b> *₹28,000/- +HRA as per rules

- # (i) ₹31,000/- + HRA to Scholars who are selected through-
- National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
  - A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions.
- (ii) ₹25,000/- + HRA for others who do not fall under (i) above.
- \* (i) ₹35,000/- + HRA to Scholars who are selected through-
- National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
  - A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions.
- (ii) ₹28,000/- + HRA for others who do not fall under (i) above.

**Note: If any candidate wishes to apply for two or more projects or different positions in a project, then such candidates have to apply separately.**

Interested candidates fulfilling the above qualifications must apply through online portal available on CEERI website. A print out of the Application Form must be taken by the candidate after successful submission of the form. The shortlisted candidates will be informed through E-mail/Institute website along with the date and time for interview. Candidates are requested to frequently browse the institute website. The eligible candidates must appear for interviews through “Microsoft Teams” (MS Teams). Candidates will be allowed as a Guest in MS Team, for which the link will be send to your registered email. The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including marksheets and certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

The last date & time for applying applications is 22.09.2022 till 05:00 P.M. Application received after last date will not be entertained.

<p><b>For recruitment queries:</b></p> <p><b>Sh. Rajan Tirkey</b>  <b>Section Officer</b>  <b>CSIR-CEERI, Pilani</b>  <b>Email: <a href="mailto:recruitment@ceeri.res.in">recruitment@ceeri.res.in</a></b>  <b>Phone: 01596 – 252275</b></p>	<p><b>For technical queries:</b></p> <p><b>Sh. Arvind Kumar Khandelwal</b>  <b>Network Management Cell</b>  <b>CSIR-CEERI, Pilani</b>  <b>Email: <a href="mailto:arvindkumar@ceeri.res.in">arvindkumar@ceeri.res.in</a></b>  <b>Phone: 01596 – 252282</b></p>
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### Other Conditions:

- Candidates who have already served CSIR-CEERI or any other lab/ institute of CSIR as Project Staff such as Project Assistant/ Project Fellow/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate etc. for a total period of 5 years or more are not eligible for these engagements. The candidates who have served for a period less than 5 years will have tenure upto remaining period till completion of five years.
- Tenure of engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research Associate- I, Project Assistant, Project Associate-I/II, JRF/SRF etc. shall not exceed 5 years in any case. The total tenure of five years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/ or any other Lab./ Instt. of CSIR as RA, Project Assistant, Project Associate Level II/III etc. or as any other designation of equal status.
- The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
- The engagement of Project Assistant, Project Associate- I/II, SPA, SRF, JRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.
- Reservation:** As regards reservation, if all things are equal, SC/ST/OBC/PH/EWS candidates may be given preference over General candidates so as to ensure their representation.
- Relaxation in age limit for SC/ST/OBC/PH and women shall be 5 years.
- Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:-**
  - Candidates shall upload scanned copies of following academic certificates while applying for project staff positions:-
    - SSLC/10th
    - HSC/Senior Secondary/Intermediate/12th
    - Graduation Degree (B.A./B.Com./BBA/B.Sc. /B.E. /B.Tech. etc.)
    - Post Graduation (M.Sc./M.E./M.Tech./MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification / Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/DGPA should be clearly displayed on any one of the certificate. If certificate doesn't have proper percentage / CGPA /DGPA/Grade then the application may not be considered. The candidate must ensure to provide

proper certificates to ensure their eligibility for the positions they are applying for.

- b. Date of Birth Proof (Any valid certificate having DoB such as SSLC, HSC, DoB Certificate, etc)
  - c. Category certificate (in case of SC/ST/OBC/PWD/EWS candidates)
  - d. NET/GATE/Any other equivalent exam qualification certificate (If applicable)
  - e. Experience certificate (Mandatory for the posts where experience is essential qualification)
  - f. Other certificates (If applicable)
8. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIRCEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
  9. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. The Screening Committee will screen all applications received for the specified project and recommend eligible candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the applicant and debar for attending the interview in future.
  10. Eligible applicants may be called for interview through online and Interview letter will be sent him/her through e-mail intimating the date & time of Interview, link/reference of online mode, terms & conditions, if any. The candidate will be allowed to join only after verification of original certificates and other information found correct.
  11. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
  12. CSIR-CEERI reserves the right to cancel or withdraw the Offer of engagement in case of any discrepancy found, in the candidature of any empanelled candidate at any stage.
  13. The number of positions may be increased or decreased as per the requirements.
  14. Other terms & conditions will be governed as per guidelines issued by the funding agency/ CSIR/ CEERI for the engagement of above Project Staff as amended from time to time.

**Sd/-**  
**CONTROLLER OF ADMINISTRATION**